

Flexibel Fluorosence Intensity Measurement Platform

PROJECT PLAN

within the lecture of UppSense

at Uppsala University in the Departement of Information Technology

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Contents

1	Device Description	1
	1.1 System Architecture	1
	1.2 Realization	1
	1.3 Frabrication	2
2	Budget	3
	2.1 Prototype	3
3	Next steps	4
	3.1 Interface to the Assay Team	4
	3.2 First Experiments	
	3.3 Lot of Programming	5
4	Gantt	6

List of Figures

1.1	Rough sketch of the system architecture	1
3.1	Spectral characteristics of the two fluorescent dyes, which have been targeted so far. $\ . \ .$	4
4.1		6

List of Tables

2.1	Rough budget estimation	for one prototype.								3
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1 Device Description

System Architecture

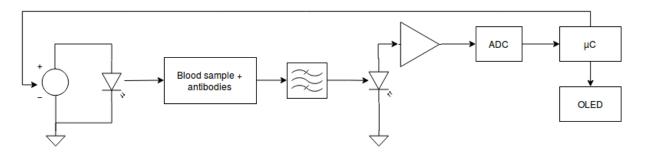


Figure 1.1: Rough sketch of the system architecture.

In figure 1.1 one can see a rough sketch of the system architecture, which is planned. The signal flow is from the left to the right. First the assay has to be excited, which is done by a voltage source connected to a light emitting diode (LED). This can also be another voltage controlled source of light. Hence this has TBD. The light source however is controlled by software (Microcontroller, μ C), which is done by a simple e.g. transistor circuit. Using this approach has the advantage, that one can mix up the received signal to a higher band by switching the light source on and off with a rectangular signal of e.g. 1 kHz. By doing so one can suppress noise. In this case one simply compares the average value when turned on to the average value when turned off. The difference is the value one is looking for.

The likelihood, that the emitted light has to be filtered is quite high. Hence one needs an optical filter. Behind the filter a photo diode is located, which converts the light signal to an electrical signal. This signal probably has to be processed in a analog way (e.g. amplification and filtering) before it is transfered in the digital world (analog-digital converter, ADC). In the digital world one has a lot of possibilities.

The UI will be a cheap OLED display from Chine, which can be extended by buttons. In a later revision maybe bluetooth can be used to transfer the data to a mobile device.

Realization

The idea is to have a printed circuit board (PCB) as main part of the device in the end. On this board the light source as well as the sink shall be placed on the bottom side. On the top side one can mount the μ C and the UI. A USB jack shall be used as power source. So one can easily use a power bank or a laptop to operate the device.

The optical filter can be mounted with epoxy resin on the PCB. Spacing bolts can be used to attach the PCB to a bottom plate, where one can find a mechanism to easily fix the blood probe chip. Futhermore a black box which one can put over the so far depicted structure, has to be designed. This black box supresses light (noise) from the environment. Mechanical sketches, preferably created with a 3D CAD software, have to be provided in a later revision of this document. The mechanics can be 3D printed in house.



Frabrication

The PCBs can be fabricated in China. This is an example of a manufacturer: https://www.elecrow. com/10pcs-2-layer-pcb.html. The lead time until one can actually work with designed PCBs is roughly three weeks including the assembling and soldering, which has to be done by the egineering subgroup. Moreover simpler PCBs can be etched at the university.

2 Budget

Prototype

Item	\mathbf{Costs}	Description
PCBs	50 \$	5 cm x 5 cm, cheapest option, which still has an acceptable quality
Components	$100 \$	one needs to order components for the PCBs (diodes, ICs, µCs, resistors, etc.)
Soldering	$0 \$	can done by the engineering subgroup
3D printing	0	for the mechanical stuff, can probably be done by Uwe
Assembling	0 \$	can be done by the engineering subgroup
\sum	150 \$	

Table 2.1: Rough budget estimation for one prototype.

It is so far quite hard to estimate the exact product price as a lot of choices concerning the hardware have not been made. In the end when we know with which components one device is built up we can calculate a product price depending on how many devices are produced.

3 Next steps

Interface to the Assay Team

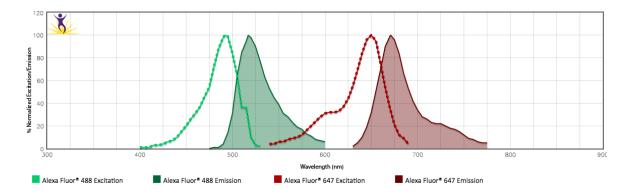


Figure 3.1: Spectral characteristics of the two fluorescent dyes, which have been targeted so far.

The assay team has so far targeted two different products, which they want to use to generate fluoresence light emission depending on the concentration of the so-called NT-ProBNP. It is now the task of the egineering team to put the required hardware together for the given spectral features. The two main parts as already emphasized above are the light emission and the light reception. For each product one has to set up a series of experiments to play with different parameters. For the first attempts this only includes analog parts and an oscilloscope. The challenge is mainly, that we do not know so far how much light will be emitted. Is it visible? Is it a few photons? Also we do not know how constant the light emission for exitation has to be: Do we need an extra controller for that? Many lasers for instance provide an internal receiving photodiode, which is highly coupled with the sending diode. So one can use a software controller to keep the light emission pecisely at one point. Moreover we do not know so far how dark it should be arround the sample.

First Experiments

One needs to design a circuit with optics, which is delivering an analog voltage at the output, which is dependent on the concentration of NT-proBNP. To build this circuit one has to determine which components are used also one has to copare different standard approaches how to monitor a photo diode with an Opamp. The thing, which has to be figured out in the end is, which gain is required, that the maximum and minimum light emission is visible as an electrical signal. Also the maximum output voltage should be equal to the reference voltage and the minimum output voltage should be 0 V to get the highest resolution according to signal processing theory. This requirements might result in more than one amplification stage. To develop this circuit LTSpice will be used as well as KiCAD. Most likely this PCB will be etched within the university. As soon as these experiments work, one can start working with a microcontroller.

The experiments can be carried out together with the biomedical colleagues in a dark room with and oscilloscope and a signal generator. Maybe three different circuits will be put on one PCB to make a simultaneous reception possible.



Lot of Programming

As soon as we found an essay and a corresponding analog circuit, which works a microcontoller will be chosen and everything will be put together on one board. This microcontroller could be the NRF52832, MSP432 or a simple ATmega328P. The data has to be sampled and some calculations have to be done. Finally the data has to be displayed on an OLED. One important requirement for the whole team is, that the device is small as it has to be transported to Eindhoven for the competition in december.

4 Gantt

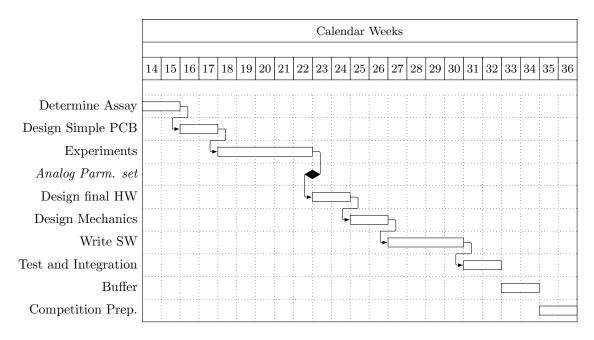


Figure 4.1: Gantt Chart

This chapter is about the current time plan. The experiments are mainly to figure out the correct amplifier design and to determine if the desired technique works at all. As soon as the parameters for the amplifier are set the device design phase starts. And the final hardware is designed. At the end there is a buffer of three weeks if anything goes wrong. If we can stick to this ambitious timeplan we may take this period off. The last two weeks are reserved for preparing the competition as e.g. a presentation and a stand have to be prepared.