

MAXIMILIAN STIEFEL

Teach lead, electrical engineer & programmer

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Born 05.04.1994 in Stuttgart, Germany Website Side projects



EMPLOYMENT

Project Manager

BAE Systems Hägglunds

Since October 2022

Örnsköldsvik

Technical Program Manager: Leading all technological development of the CV90 export variant for Switzerland (Schützenpanzer 2000), overseeing both the midlife upgrade (NUV - Nutzungsverlängerung) and maintenance/modification projects. Budget: approx. SEK 250e6.

Subsystem Manager: Responsible manager for a subsystem in all CV90 IFVs, the Diagnostic Information System (DIS), guiding the software team in rapid feature development for multiple projects while addressing bug reports via Scrum's fortnightly releases. Support engineers in creating customized DIS hardware for each export version. Budget: approx. SEK 40e6.

NL MLU

CH NUV

FIN MLE

ISO 26262

Embedded Engineer

Tecan

September 2018 - September 2022 Männedorf

Software Engineer: In charge of requirements, work estimation, SW architecture / detailed design, implementation, integration, CI/CD, V&V support / guidance, risk management (safety classification), software release (git), release documentation and transfer to production of a 50k LOC C++ firmware platform running on a dozen different hardware modules.

IEC 62304

cLLD

Fluent® Automation Workstation

Electronic Engineer: Primarily responsible electronic engineer for the development of a new liquid handling robotics platform optimized for PCR involving the design of the entire cable tree, PCBs and entire modules, safety and EMC testing, communication with suppliers, evaluation and choosing of 3rd party devices, test fixture fabrication comprising hardware and software.

Cavro® Magni Flex

BLE

Altium

ARP

Interdisciplinary team

5G Researcher

Ericsson Research

January 2018 - August 2018

Stockholm

Contributing to 5G: Implementation of Narrowband IoT as an SDR utilizing the C++ framework GNURadio.

3GPP

IoT

SIMD

OFDM

EDGE

MTC

Development Engineer / "Dualer Student"

Airbus Defence and Space

September 2013 - September 2016 Immenstaad

"Video over Ethernet" on a FPGA: UDP-based protocols for video transmission on the department's flagship graphics card. Map generation devices for helicopters: HW and low-level SW design.

IGLOO2 FPGA

VHDL

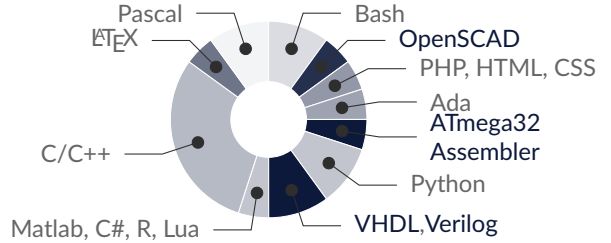
Designspark

Bare-metal C

FREE-TIME ACTIVITIES

Running Weight training Tinkering Hiking Renovating my cabin

PROGRAMMING/DIGITAL DESIGN



EDUCATION

M.Sc. in Embedded Systems

Uppsala University, Sweden

August 2016 - August 2018

B.Eng. in Electrical Engineering

Baden-Württemberg Cooperative State University, Friedrichshafen, Germany

September 2013 - September 2016

Specialization: Communication Engineering

LANGUAGES

German

English

Swedish

Dutch

French



MOST PROUD OF

December 2023: Demonstrating strong capabilities in maximizing the product's value for the customer armasuisse while getting paid for it and being subsequently promoted.

July 2020: One of the inventors of a Tecan patent. Background: Augmented reality w/ eye tracking.

March 2018: My first commit is accepted and becomes part of the (perhaps) most-popular SDR development platform, GNURadio.

May 2017: Main author of a scientific publication about ultra low-power wireless communication presented at the Swedish National Computer Networking Workshop 2017 (SNCNW).

November 2017: My team and I won the Swedish Embedded Award (25 000 kr) with our biosensor at the Embedded Conference Scandinavia.

March 2013: First place in the technology category in the biggest youth science and technology competition in Europe "Jugend forscht", Friedrichshafen, Germany.